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07/16/2003

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	William M	Ha	nlon. Jr		(Depositor's name)		
┙	11111-	1an	lors	7	(Signature)		
	September				(Date)		

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/001,668	10/23/2001	Martin Gehrke	DFS-138-A	2075

TITLE OF INVENTION: DESIGN FOR CONSTRUCTING AN INPUT CIRCUIT TO RECEIVE AND PROCESS AN ELECTRICAL SIGNAL

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE	
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Our Reference: DFS-138-A PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Martin Gehrke & Torsten Pechstein

Serial Number: 10/001,668
Filing 'Date: October 23, 2001
Examiner/Art Group Unit: I.B. Patel/2827

Title: A DESIGN FOR CONSTRUCTING AN INPUT

CIRCUIT TO RECEIVE AND PROCESS AN

ELECTRICAL SIGNAL

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